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**AXIOM INTRODUCES INDUSTRY'S FASTEST
MULTI-CPU SYSTEMVERILOG SIMULATOR**

MILPITAS, Calif. May 16, 2005 – AXIOM Design Automation, formerly @HDL Inc., today announced the release of a complete SystemVerilog verification environment, augmenting its existing leading edge formal verification and debugging products with the introduction of the MPSim™ simulator. MPSim offers native design, testbench, and assertions while delivering up to 6x performance improvement using commercially available multi-CPU hardware.

The emerging computer hardware and CPU technology is focused on delivering multi-core and multi-CPU systems rather than the previous emphasis on faster clock speeds. “MPSim is a fourth generation simulator architected from the ground up to take advantage of these emerging hardware platforms,” said Badru Agarwala, President and CEO of AXIOM. “MPSim can achieve up to 6x performance across all aspects of verification with currently available commercially hardware and without requiring any changes to the design or verification environment.” MPSim is architected such that its performance will scale further as hardware with more than 8 CPU's become available.

“The AXIOM team has extensive and proven experience in developing simulation technology,” said Dr. Prabhu Goel, AXIOM board member and founder of Gateway Design Automation. “MPSim's patent pending application of multiprocessor technology offers the quantum jump in productivity necessary for addressing the verification bottleneck faced by companies taping out complex SoC's.”

MPSim also provides a unique graphical environment that allows integrated debugging of design, testbench and assertions to help locate and fix the most complex design bugs. The overall performance of the verification environment is enhanced by native integration of testbench and assertions. MPSim is currently in production use at customers with leading edge designs including Raza Microelectronics, SiNett and Bay Microsystems.

Customers can visit AXIOM at the Design Automation conference to see a first hand demo of these exciting new products or they can visit the AXIOM website at www.axiom-da.com .

About AXIOM

Originally founded as @HDL in 1999, AXIOM has been a pioneer in developing functional verification products. The company's mission has always been to accelerate the verification flow and to increase the overall productivity of verification engineers. The introduction of the MPSim multi-CPU simulator provides a quantum leap in verification performance to the industry and enables AXIOM to offer a complete solution to its customers.

AXIOM's integrated verification environment combines the fastest simulator in the industry with advanced testbench automation, assertion based verification, debugging and coverage analysis to ensure that your most complex designs are fully verified in the least amount of time. AXIOM supports commercially available hardware and industry standard languages including SystemVerilog, Verilog, OpenVera and PSL to ensure continuity with your existing verification methodology while offering a clear path to immediate productivity improvement.

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